In re application of:

RADRAS Sishi NAGABA et al.

Serial No: 09/965,534

Filed: September 25, 2001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Art Unit:

2826

Examiner:

ABRAHAM, Fetsum

I hereby certify that this correspondence being deposited with the United States Post Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents Washington D.C. 20231, on

December 5, 2002 te of Deposit

r Reg. No. 41,232

Date

Box Non-Fee Amendment Commissioner for Patents Washington, D.C. 20231

Dear Sir:

Transmitted herewith is an amendment in the above-identified application.

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

 \boxtimes No additional fee is required.

A check in the amount of &

The fee has been calculated as shown below:

	(Col. 1) CLAIMS REMAINING AFTER AMENDMEN		(Col. 2) HIGHEST NUME PREVIOUSLY PAII		(Col. 3) PRESENT EXTRA*	LG/SM \$ ENTITY I			D'L DUE
TOTAL CLAIMS FEE	13	-	20	**	0	LG=\$18 SM=\$9	\$	\$	0
INDEPENDENT CLAIMS FEE	3	-	3	***	0	LG=\$84 SM=\$42	\$	\$	0
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIMS LARGE ENTITY FEE = \$280 SMALL ENTITY FEE = \$140									
* 16 the entry in Onl 4						TC	OTAL	\$	0

If the entry in Col. 1 is less than the entry in Col. 2, write "0" in Col. 3.

If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, write "20" in this space.

If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, write "3" in this space. The "Highest Number Previously Paid For" (Total or Independent) is the highest number found from the equivalent box on Col. 1 of a prior amendment or the number of claims originally filed.

to payon the selection

 enclosed.	φ <u>υ</u> ιο	cover the additiona	al claims fee is enclose	d. A copy of this sheet is
A check in the amount of	of \$ <u>0</u>	to cover the exter	nsion fee is enclosed.	A copy of this sheet is
enclosed.				

The Commissioner is hereby authorized to charge any deficiencies of fees associated with this 冈 communication or credit any overpayment to Deposit Account No. 50-1314. A copy of this sheet is enclosed.

Any filing fees under 37 C.F.R. § 1.16 for the presentation of extra claims X

Any patent application processing fees under 37 C.F.R. § 1.17

Date: December 5, 2002

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HOGAN.

Anthony J.

Registration No. 41,232

Attorney for Applicant(s)



81790.0219 IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Katsushi NAGABA et al.

Serial No: 09/965,534

Filed:

September 25, 2001

For:

SEMICONDUCTOR INTEGRATED

CIRCUIT DEVICE

AMENDMENT UNDER 37 C.F.R. §1.111

BOX NON-FEE AMENDMENT

Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

Art Unit: 2826

Examiner: Fetsum Abraham

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed

Commissioner for Patents Washington D.C. 20231, on

December 5, 2002 Date of Deposit

Miler, Reg. No. 41,232

In response to the Office Action dated September 6, 2002, please amend the above-identified application as follows. Please note that in compliance with the new rules under 37 C.F.R. § 1.121, the amended portions are presented in "clean" form with marked-up versions provided in Appendix A, and for the Examiner's convenience, all claims remaining in the case are presented with a respective amendment status for each claim since inception of the application.

IN THE SPECIFICATION

On Page 13, line 10, please replace "PMOS N1" -- NMOS N1--.

On Page 14, line 17, please replace "AND" with --OR--.